

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1  
Filed: March 26, 2004  
Group Art Unit: 2818  
Customer No.: 76287

Serial No.: 10/810,035  
Examiner: Thinh Nguyen  
Confirmation No.: 8458

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

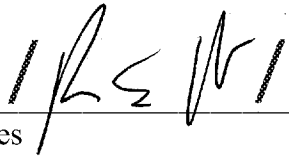
We are transmitting herewith the attached:

- ☒ Request for Refund of Issue Fee (1 pg.)
- ☒ Copy of Filed Form PTOL-85 (1 pg.)
- ☒ Copy of Request to Apply Previously Paid Issue Fee (1 pg.)

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Schwegman, Lundberg & Woessner, P.A.  
Customer No.: 76287  
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By

  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 10 day of September, 2010.

Frederik Struve  
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